

Pano Lower Board Schematic

Page Description

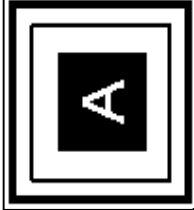
- 1Title Page
- 2RGB Video Connector
- 3USB Ports 2 and 3 Connecto
- 4Power Supplies
- 5Connection To Upper Board

INSTALLED PROPERTY

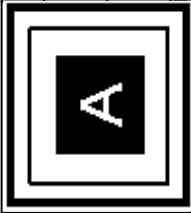
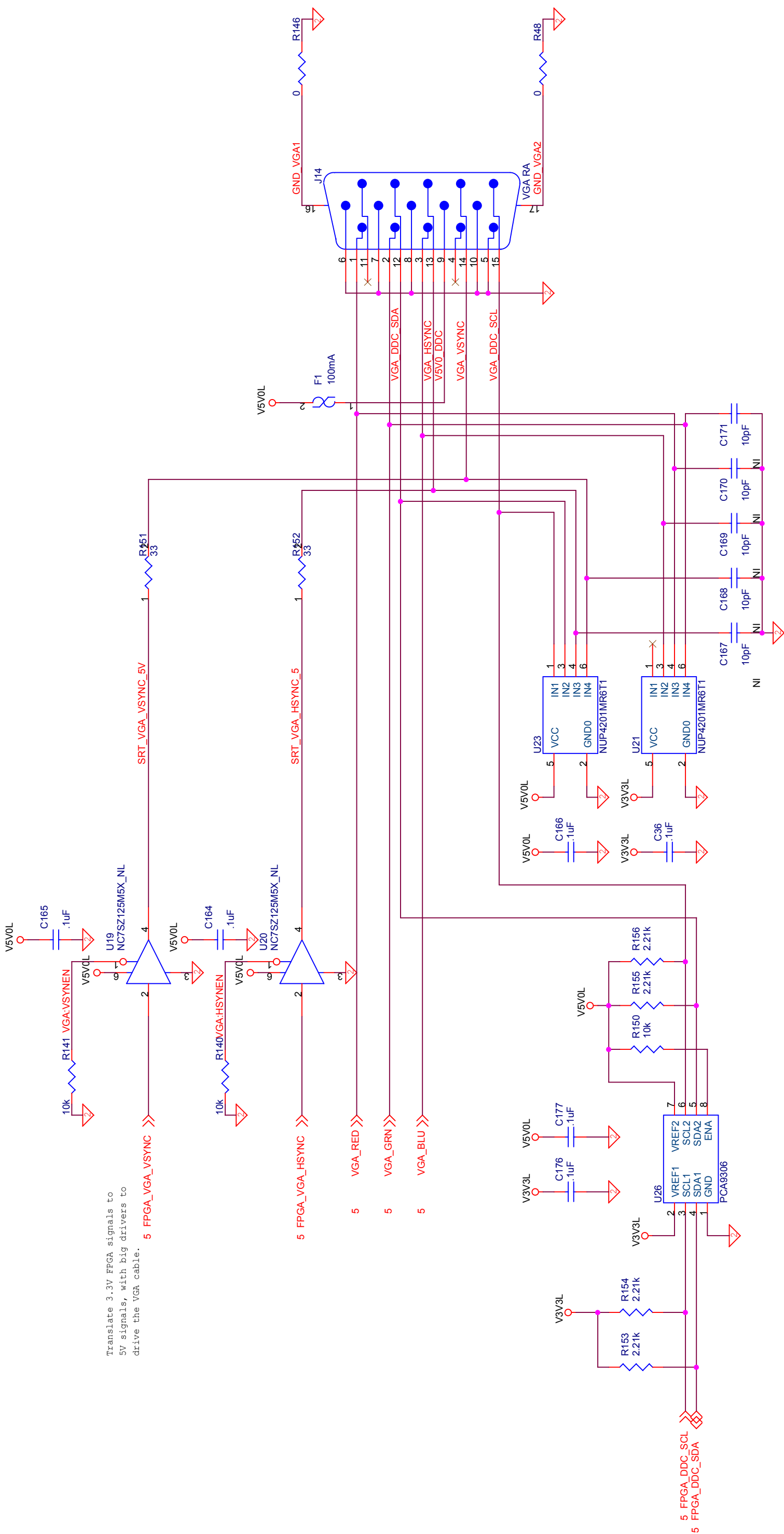
All parts on this schematic are labeled with an "INSTALLED" property. The value is one of the following:

- NullPart is installed on the board
- BlankPart is installed on the board
- NIFootprint exists on the board, but part is not installed
- PCBSchematic symbol represents a feature of the PCB, no part goes at this location.

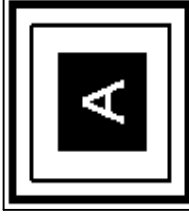
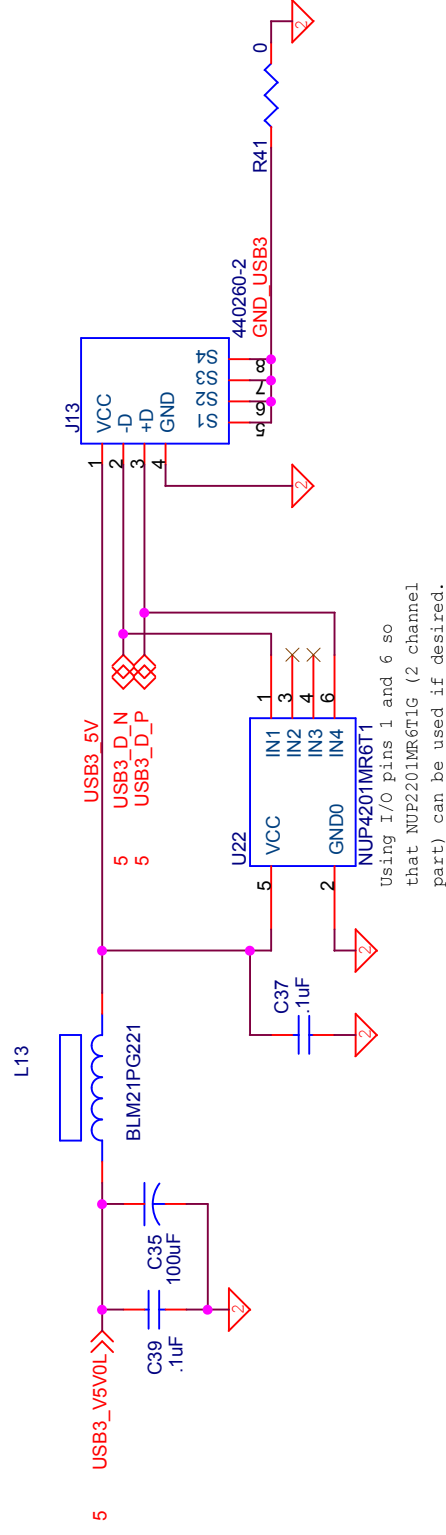
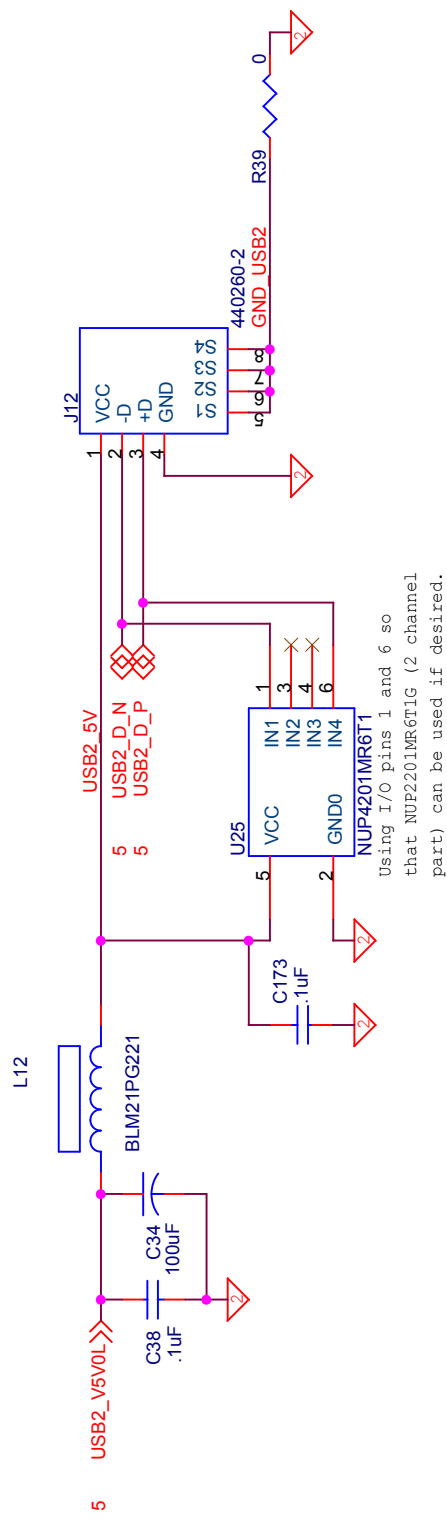
| Rev | Date | Pages | Description of Changes |
|------|----------|---------------------------------------|--|
| P4 | 7/2/07 | | Released |
| | 8/11/07 | 2 2 | Increased value of Hsync/Vsync Series Terminator Added caps to RGB, Hsync, Vsync. |
| P5 | 8/11/07 | | Released |
| P5-1 | 10/22/07 | 4 | Replaced DI7 and R50 to set the overvoltage threshold higher. |
| P6 | 11/17/07 | 3 4 2 | Revised ESD Protection Added High-Frequency Bypass to 1.2V. Swapped some pins on U21 for wide trace routing. |
| A | 11/19/07 | | Chose to call this revision "A" |
| D | 1/21/09 | 2 2 2 5 5 | Replaced FETs with PCA9306 level translator. Removed 0 ohm resistors for RGB and SYNC traces to VGA connector. Replaced 75 ohm series termination to 33 ohm for SYNC signals. Modified board to board connector pinout Added analog filters to RGB traces. |

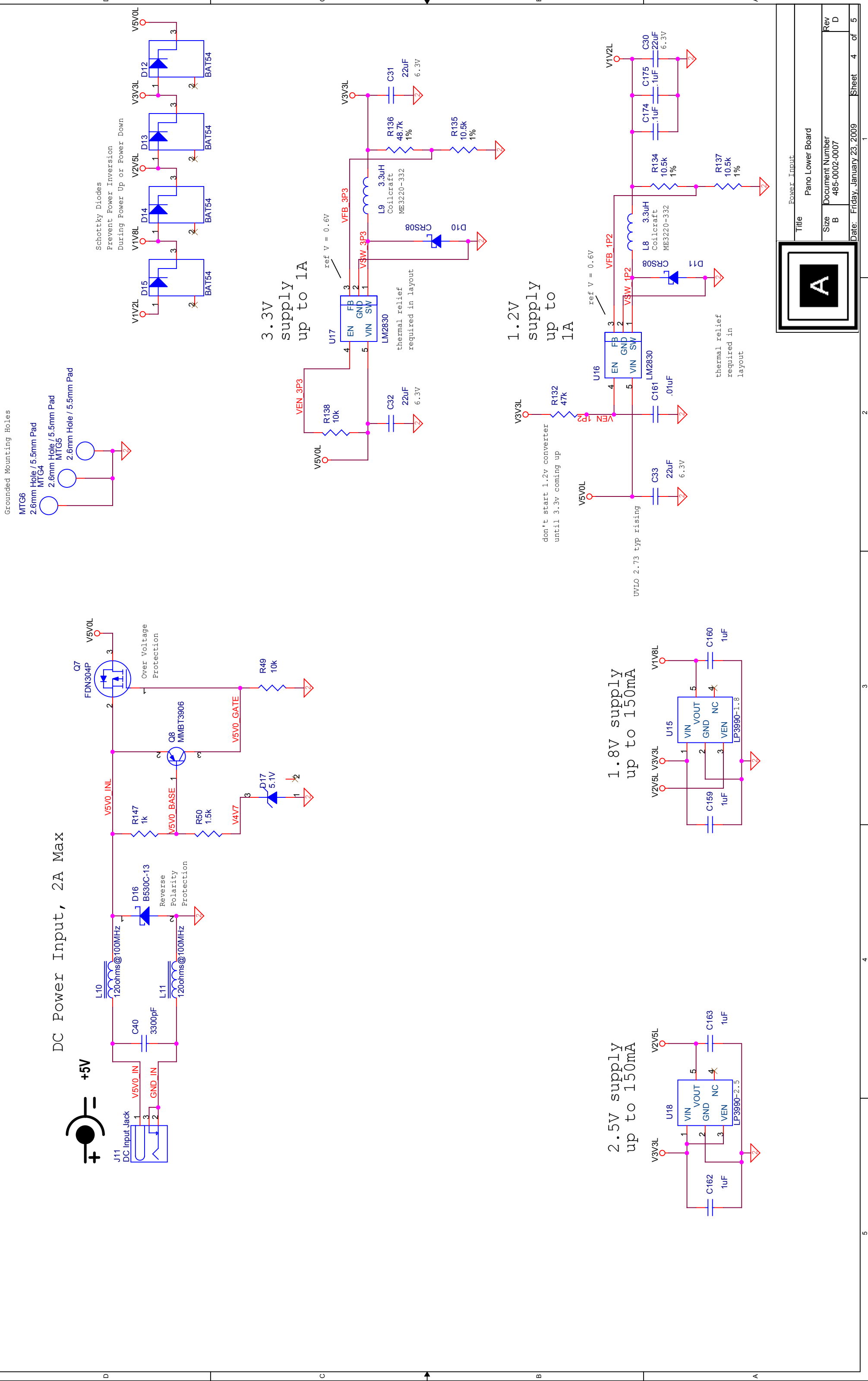


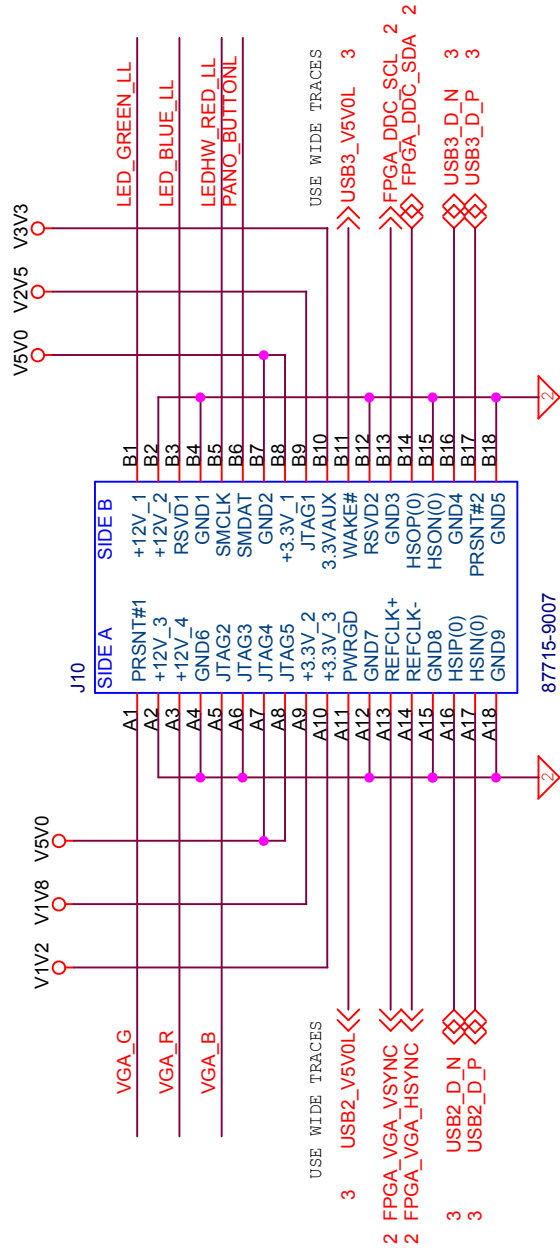
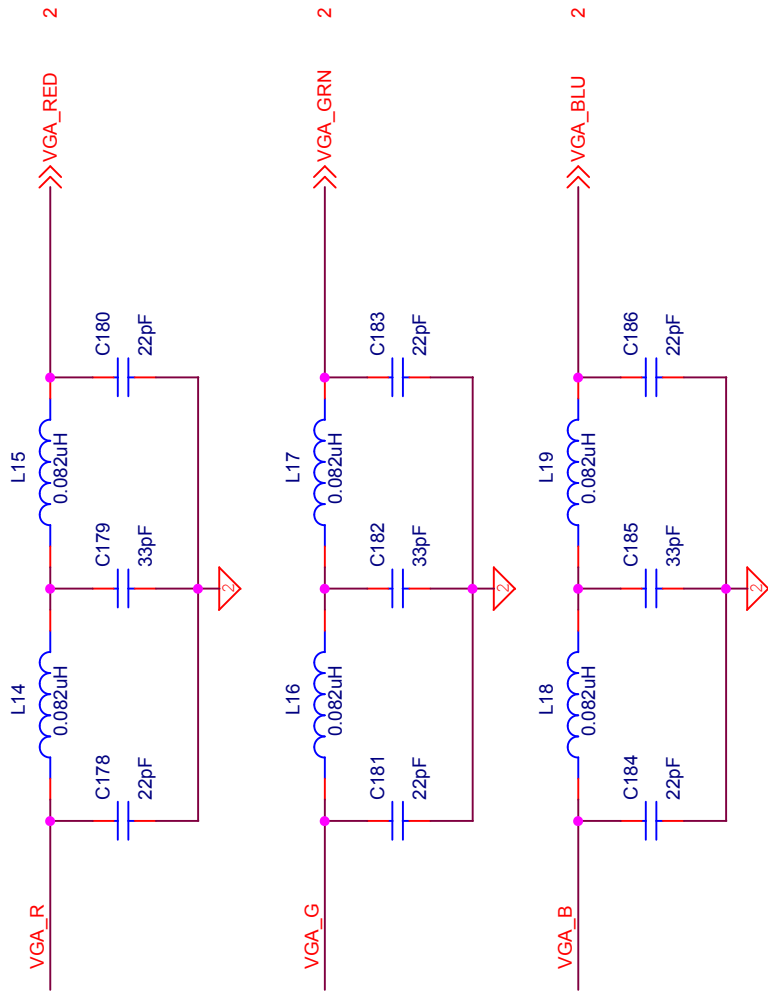
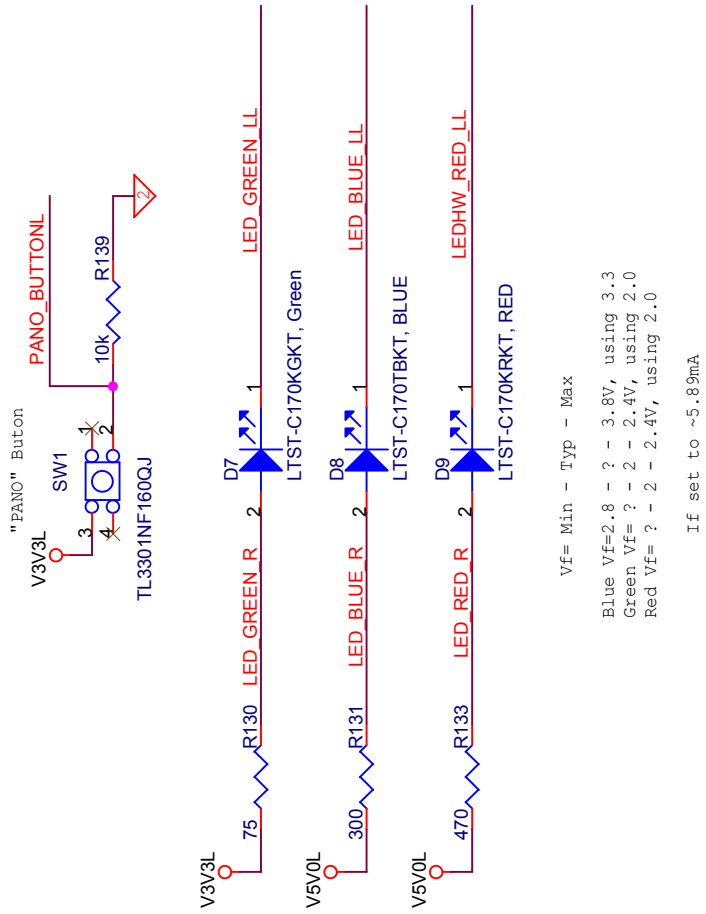
Translate 3.3V FPGA signals to 5v signals, with big drivers to drive the VGA cable.



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|-----------|--------------------------|
| VGA Video | |
| Title | Pano Lower Board |
| Size | Document Number |
| B | 485-0002-0007 |
| Rev | D |
| Date: | Friday, January 23, 2009 |
| Sheet | 2 of 5 |







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|--------------------------------|----------------------------------|------------------|--|--|--|
| A | | | | | |
| Connection To Upper Board | | | | | |
| Title | | Pano Lower Board | | | |
| Size B | Document Number 485-0002-0007 | Rev D | | | |
| Date: Friday, January 23, 2009 | Sheet 5 of 5 | | | | |